IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (Attorney Docket No. 15164US01)

In re Application of:
Shiv Kumar Gupta

Serial No. 10/685,762

Filed: October 15, 2003

Group Art Unit: 2128

For: Parallel Instances of A Plurality
Of Systems On Chip In Hardware
Emulator Verification

EXAMINER: GEBRESILASSIE, KIBROM K

CONFIRMATION NO. 6313

RESPONSE TO NON-COMPLIANT OFFICE ACTION

Mail Stop: Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper responds to the Non-Compliant Office Action dated April 17, 2009, and to the Office communication mailed 9/8/08, in the above-identified application. The Applicant respectfully requests consideration of the following remarks.

A Claim Listing begins on page 2 of this paper.

Remarks begin on page 3 of this paper.